

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 01997-242001	Application No. 09/300,641
Information Disclosure Statement by Applicant (Us s veral sheets if necessary) (37 CFR §1.98(b))		Applicant Arvind Mithal et al.	
		Filing Date April 27, 1999	Group Art Unit 2784

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,749,095	05/05/98	Hagersten			
	AB	5,778,423	07/07/98	Sites et al.			
	AC	5,829,025	10/27/98	Mittal			
	AD	5,860,126	01/12/99	Mittal			
	AE	5,873,117	02/16/99	Hagersten et al.			
	AF	5,887,138	03/23/99	Hagersten et al.			
	AG	5,893,144	04/06/99	Wood et al.			
	AH	6,038,642	03/14/00	Arimilli et al.			

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Examiner Initial	Desig. ID	Document
	AI	Aditya et al., "Semantics of pH: A parallel dialect of Haskell", Computation Structures Group Memo, 377-1, June 7, 1995
	AJ	Adve et al., "Weak Ordering - A New Definition", IEEE, 1990
	AK	Adve et al., "A Unified Formalization of Four Shared-Memory Models", Computer Sciences Technical Report #1051, 1992
	AL	Adve et al., "Shared Memory Consistency Models: A Tutorial", IEEE, September 1995
	AM	Adve et al., "A Retrospective on 'Weak Ordering - A New Definition'", First 25 International Symposia on Computer Architecture, ACM Press, 1998
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	AT	Falsafi et al., "Application-Specific Protocols for User-Level Shared Memory", Supercomputing, November 1994
	AU	Frigo et al., "Computation-Centric Memory Models", Proceedings of the 10 th ACM Symposium on Parallel Algorithms and Architectures (SPAA), June 28-July 2, 1998
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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Other Documents (include Author, Title, Date, and Place of Publication)		
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	AV	Gao et al., "Advanced Compilers, Architectures and Parallel Systems", ACAPS Technical Memo 78, December 31, 1993
	AW	Gao et al., "Location Consistency - a New Memory Model and Cache Consistency Protocol", CAPSL Technical Memo 16, February 16, 1998
	AX	Gharachorloo et al., "Memory Consistency and Event Ordering in Scalable Shared-Memory Multiprocessors", The Proceedings of the 17 th International Symposium on Computer Architecture, pages 15-26, May 1990
	AY	Gharachorloo et al., "Two Techniques to Enhance the Performance of Memory Consistency Models", Computer Systems Laboratory, 1991
	AZ	Gharachorloo et al., "Programming for Different Memory Consistency Models", Journal of Parallel and Distributed Computing, 1992
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	ABB	Keleher et al., "Lazy Release Consistency for Software Distributed Shared Memory", Department of Computer Science, March 9, 1992
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	AEE	Mukherjee et al., "Using Protection to Accelerate Coherence Protocols", International Symposium on Computer Architecture, 1998
	AFF	Ranganathan et al., "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors", Proceedings of ASPLOS-VII, October 1996
	AGG	Scheurich et al., "Correct Memory Operation of Cache-Based Multiprocessors", ACM, 1987
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	AII	Shen et al., "Modeling and Verification of ISA Implementations", Computation Structures Group Memo 400 (A), June 1997
	AJJ	Stenstrom et al., "An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing", Proceedings of the 20 th Annual International Symposium on Computer Architecture, May 1993
	AKK	Weber et al., "Analysis of Cache Invalidation Patterns in Multiprocessors", Proceedings of the 3 rd International Conference on Architectural Support for Programming Languages and Operating Systems, 1989

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FORM PTO-892		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09/300,641	GROUP ART UNIT 2187	ATTACHMENT TO PAPER NO.	5
NOTICE OF REFERENCES CITED				APPLICANT(S) Mithal et al.			
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
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EXAMINER			DATE		Form892ccs2106b		
Mehdi Namazi			March 12, 2002				
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05(a).)							

Notice of References Cited			Application N .		Applicant(s)	
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(See Manual of Patent Examining Procedure, Section 707.05(a).)